

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Sergey A. Velichko et al.

Title: CONCURRENT CONTROL OF SEMICONDUCTOR PARAMETRIC TESTING

Docket No.: 303.750US1

Filed: April 13, 2001

Examiner: Manuel L. Barbee



Serial No.: 09/834,751

Due Date: September 8, 2006

Group Art Unit: 2857

MS Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ Return postcard.
- ☒ Amendment and Response (15 pgs.).
- ☒ Communication Concerning Related Applications (1 pg.).
- ☒ Supplemental Information Disclosure Statement (2 pgs.), Form 1449 (1 pg.), and copy of 1 cited document.
- ☒ Authorization to charge Deposit Account 19-0743 in the amount of \$180.00 to cover the fee for consideration of Information Disclosure Statement under 97(c).

If not provided for in a separate paper filed herewith, Please consider this a **PETITION FOR EXTENSION OF TIME** for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
Customer Number 21186

By: David R. Cochran
Atty: David R. Cochran
Reg. No. 46,632

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 8 day of September, 2006.

Lisa Rosarske
Name

Lisa Rosarske
Signature

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
(GENERAL)

S/N 09/834,751

PATENT

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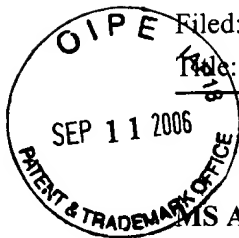
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COMMUNICATION CONCERNING RELATED APPLICATION

MS Amendment

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Applicants would like to bring to the Examiner's attention the following related application in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date/Issue Date</u>	<u>Attorney Docket</u>	<u>Title</u>
11382655	May 10, 2006	303.771US2	INTELLIGENT MEASUREMENT MODULAR SEMICONDUCTOR PARAMETRIC TEST SYSTEM

Continuations and divisionals may be later filed on the cases listed above, or cited to the Examiner in any previous Communication Concerning Related Applications. Applicants request that the Examiner review all continuations and divisionals of the above-listed or previously-cited patent applications before allowing the claims of the present patent application.

Respectfully submitted,

SERGEY A. VELICHKO ET AL.

By Applicants' Representatives,

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Date

8 September 2006

By

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David R. Cochran
Reg. No. 46,632

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Name

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